

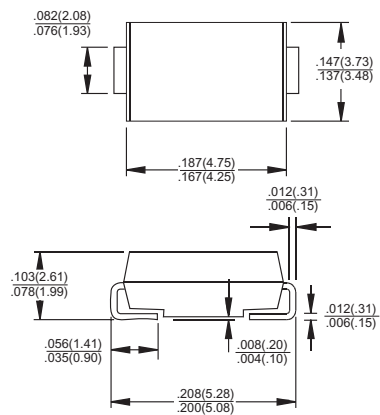


<div>TSC</div> <div></div>	<div>HS2A THRU HS2M</div> <div>2.0 AMPS. High Efficient Surface Mount Rectifiers</div>										
<div></div>	<div>Voltage Range</div> <div>50 to 1000 Volts</div> <div>Current</div> <div>2.0 Amperes</div>										
<div>Features</div> <div><div>✧ Glass passivated junction chip.</div><div>✧ For surface mounted application</div><div>✧ Low forward voltage drop</div><div>✧ Low profile package</div><div>✧ Built-in stain relief, ideal for automatic placement</div><div>✧ Fast switching for high efficiency</div><div>✧ High temperature soldering: 260°C/10 seconds at terminals</div><div>✧ Plastic material used carries Underwriters Laboratory Classification 94V-O</div></div>	<div>SMB/DO-214AA</div> <div></div> <div>Dimensions in inches and (millimeters)</div>										
<div>Mechanical Data</div> <div><div>✧ Cases: Molded plastic</div><div>✧ Terminals: Solder plated</div><div>✧ Polarity: Indicated by cathode band</div><div>✧ Packing: 12mm tape per E1A STD RS-481</div><div>✧ Weight: 0.093 gram</div></div>											
<div>Maximum Ratings and Electrical Characteristics</div> <div>Rating at 25°C ambient temperature unless otherwise specified.</div> <div>Single phase, half wave, 60 Hz, resistive or inductive load.</div> <div>For capacitive load, derate current by 20%</div>											
Type Number	Symbo l	HS 2A	HS 2B	HS 2D	HS 2F	HS 2G	HS 2J	HS 2K	HS 2M	Units	
Maximum Recurrent Peak Reverse Voltage	V <sub>RRM</sub>	50	400	200	300	400	600	800	1000	V	
Maximum RMS Voltage	V <sub>RMS</sub>	35	70	140	210	280	420	560	700	V	
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	200	300	400	600	800	1000	V	
Maximum Average Forward Rectified Current See Fig. 2	I <sub>(AV)</sub>	2.0								A	
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method )	I <sub>FSM</sub>	50								A	
Maximum Instantaneous Forward Voltage @ 2.0A	V <sub>F</sub>		1.0			1.3		1.7		V	
Maximum DC Reverse Current @ T <sub>A</sub> =25°C at Rated DC Blocking Voltage @ T <sub>A</sub> =100°C	I <sub>R</sub>	5.0 100								uA uA	
Maximum Reverse Recovery Time (Note 1)	T <sub>rr</sub>			50				75		nS	
Typical Junction Capacitance (Note 2)	C <sub>j</sub>			50				30		pF	
Maximum Thermal Resistance (Note 3)	R <sub>θJA</sub>	80								°C/W	
Operating Temperature Range	T <sub>J</sub>	-55 to +150								°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150								°C	

Notes: 1. Reverse Recovery Test Conditions:  $I_F=0.5\text{A}$ ,  $I_R=1.0\text{A}$ ,  $I_{RR}=0.25\text{A}$   
 2. Measured at 1 MHz and Applied  $V_R=4.0$  Volts  
 3. Mounted on P.C.B. with 0.4"x0.4" ( 10 x 10 mm ) Copper Pad Areas.

## RATINGS AND CHARACTERISTIC CURVES (HS2A THRU HS2M)

FIG.1- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

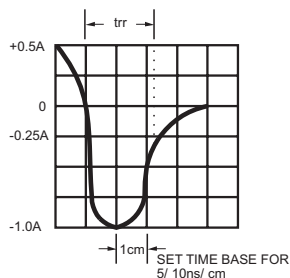
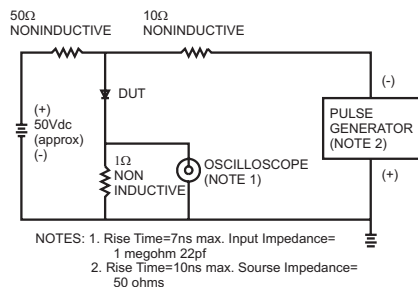


FIG.2- MAXIMUM FORWARD CURRENT DERATING CURVE

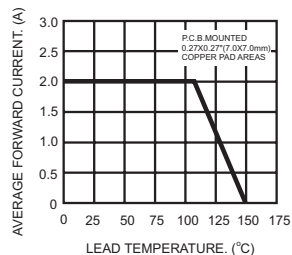


FIG.3- TYPICAL REVERSE CHARACTERISTICS

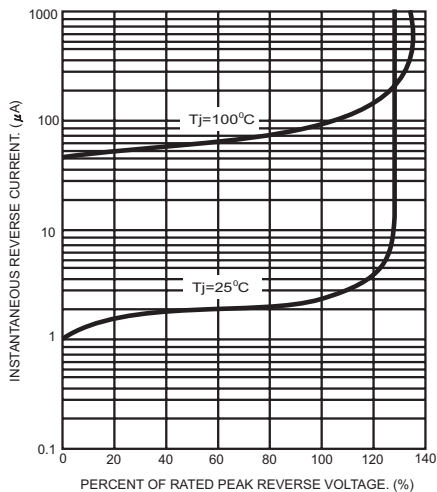


FIG.4- TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

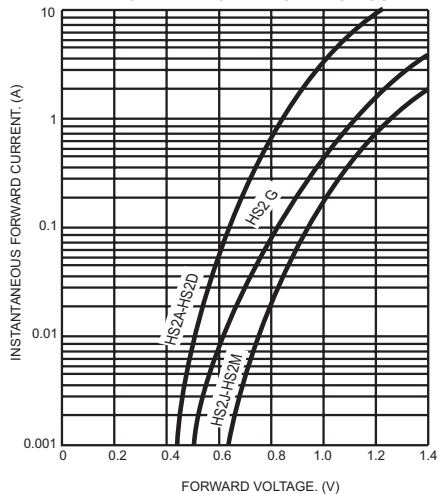


FIG.5- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

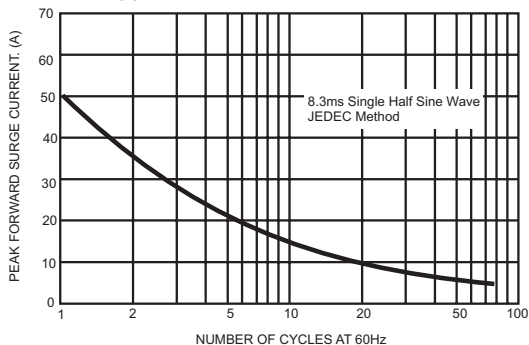


FIG.6- TYPICAL JUNCTION CAPACITANCE

